

Notice of References Cited	Application/Control No. 10/687,424	Applicant(s)/Patent Under Reexamination LIAW, JHON JHY	
	Examiner Kin-Chan Chen	Art Unit 1765	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,150,286	11-2000	Sun et al.	438/791
	B	US-			
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	D	US-			
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	F	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	✓ S.Wolf "Silicon Processing for the VLSI Era", 1990, vol. 2, pages 354-355.
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.